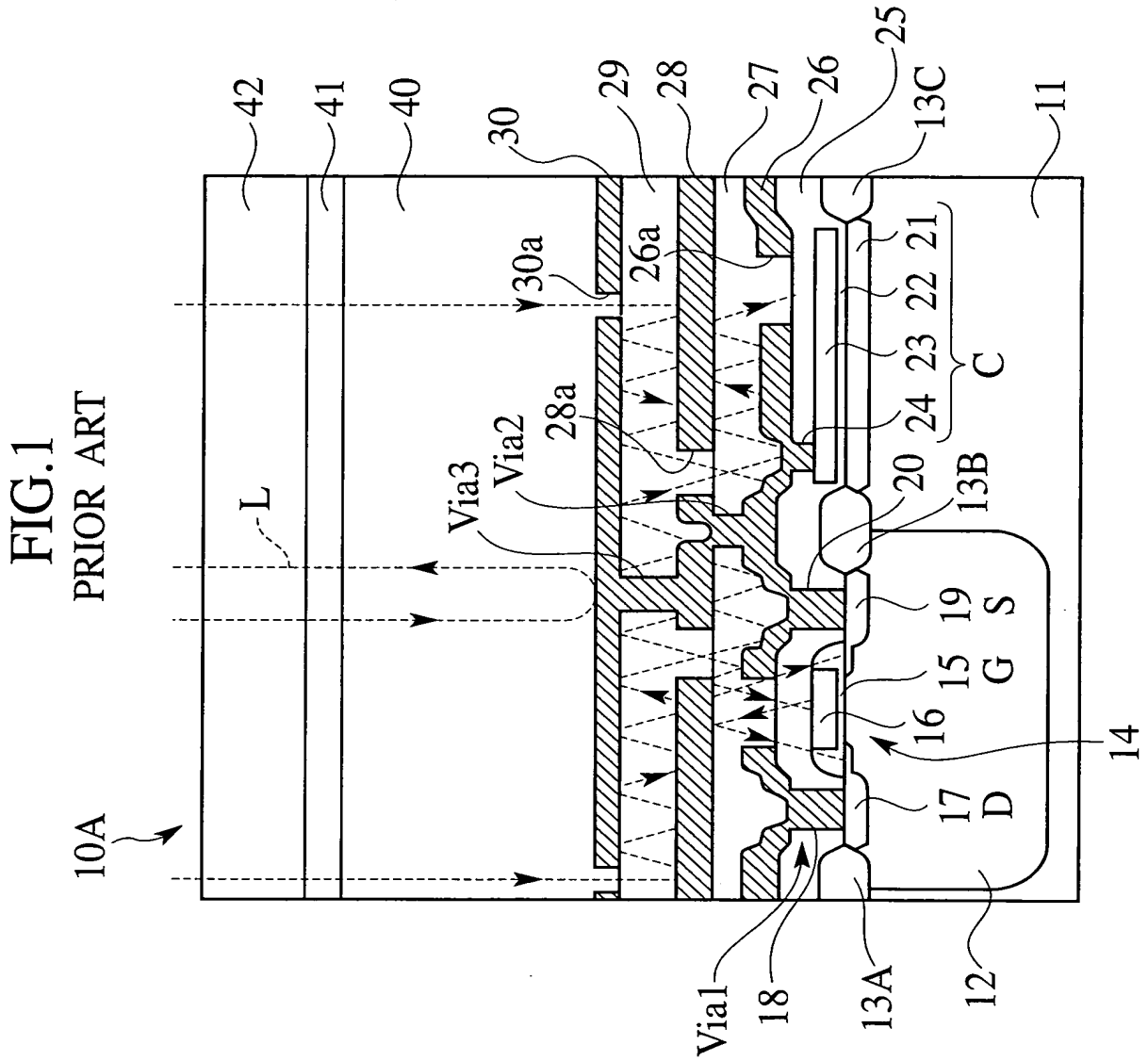


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FIG.2A
PRIOR ART

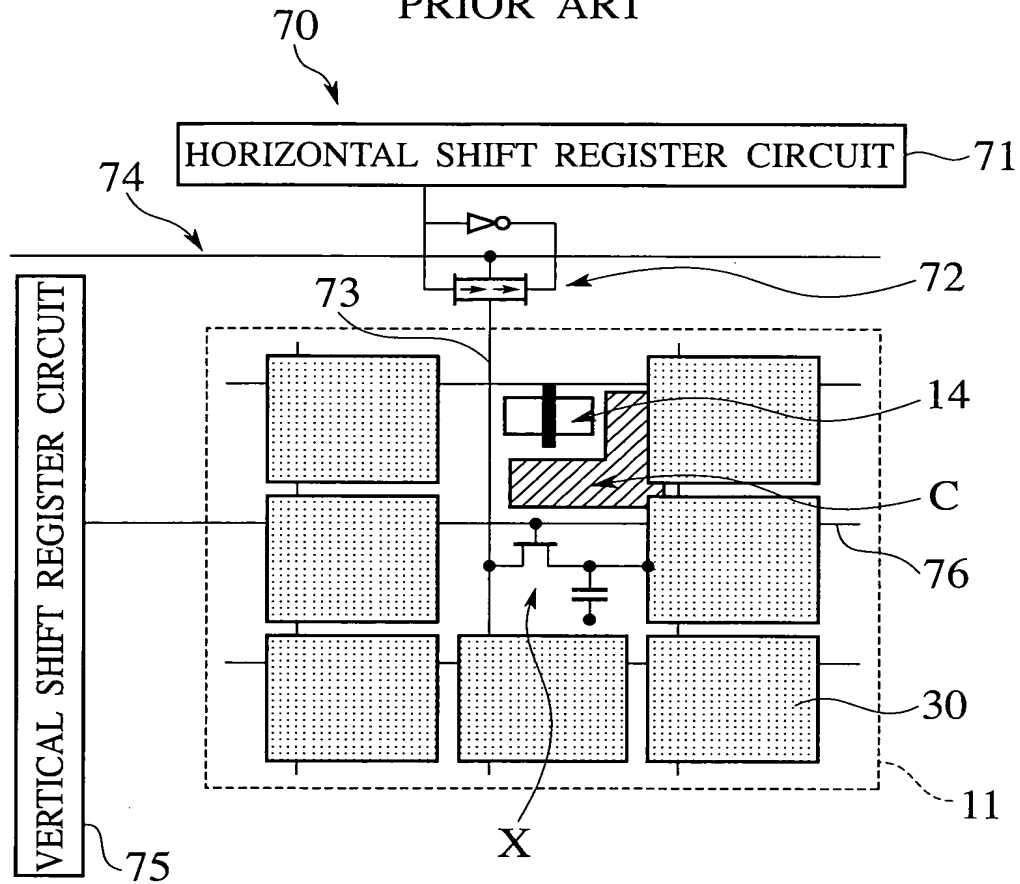


FIG.2B
PRIOR ART

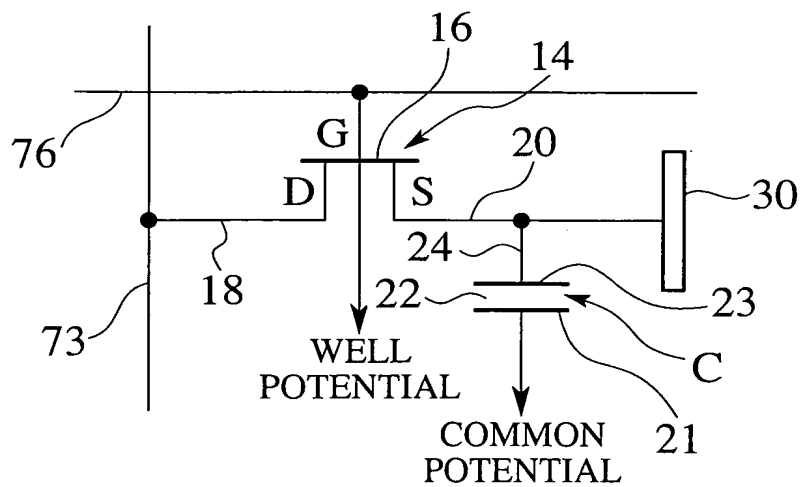
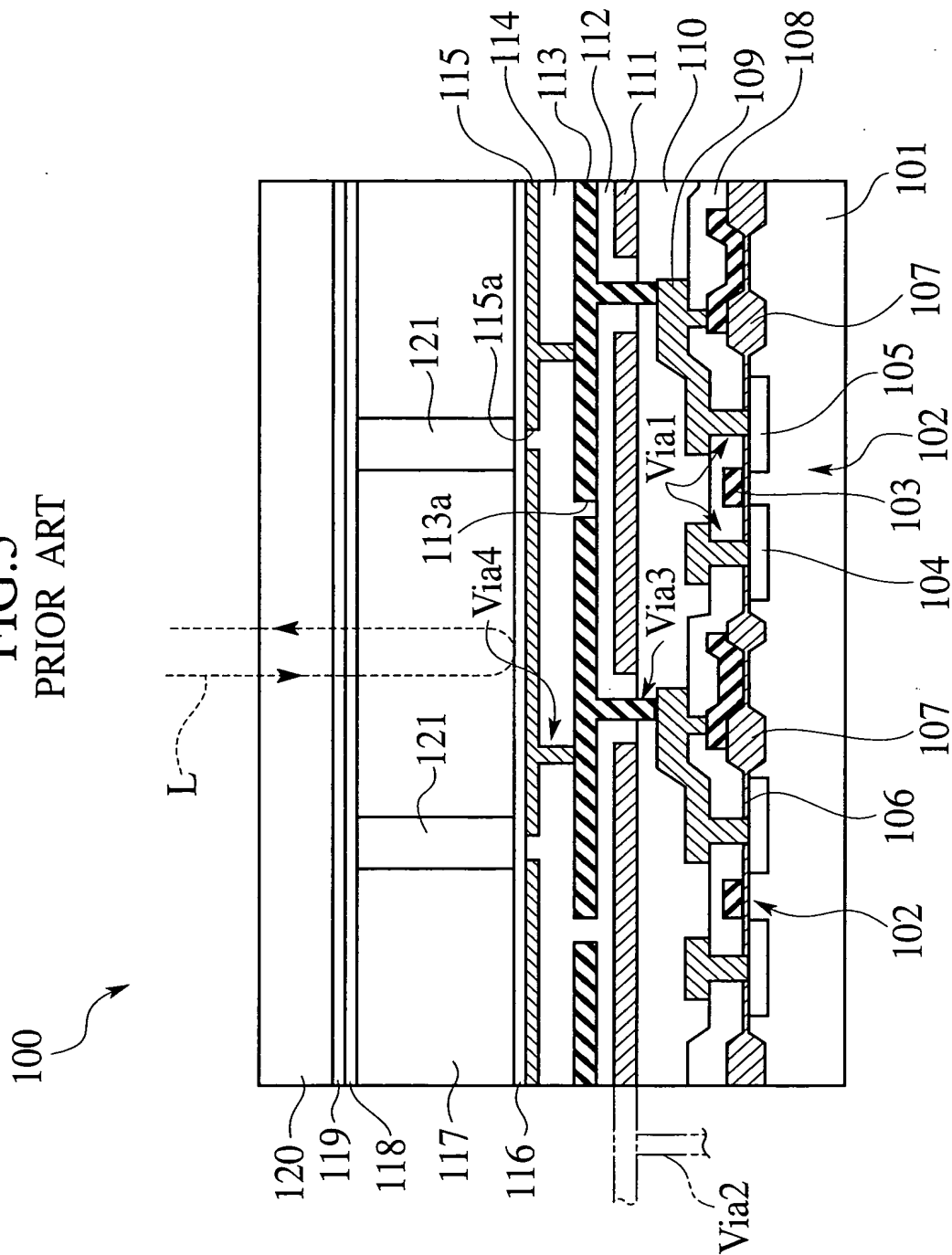
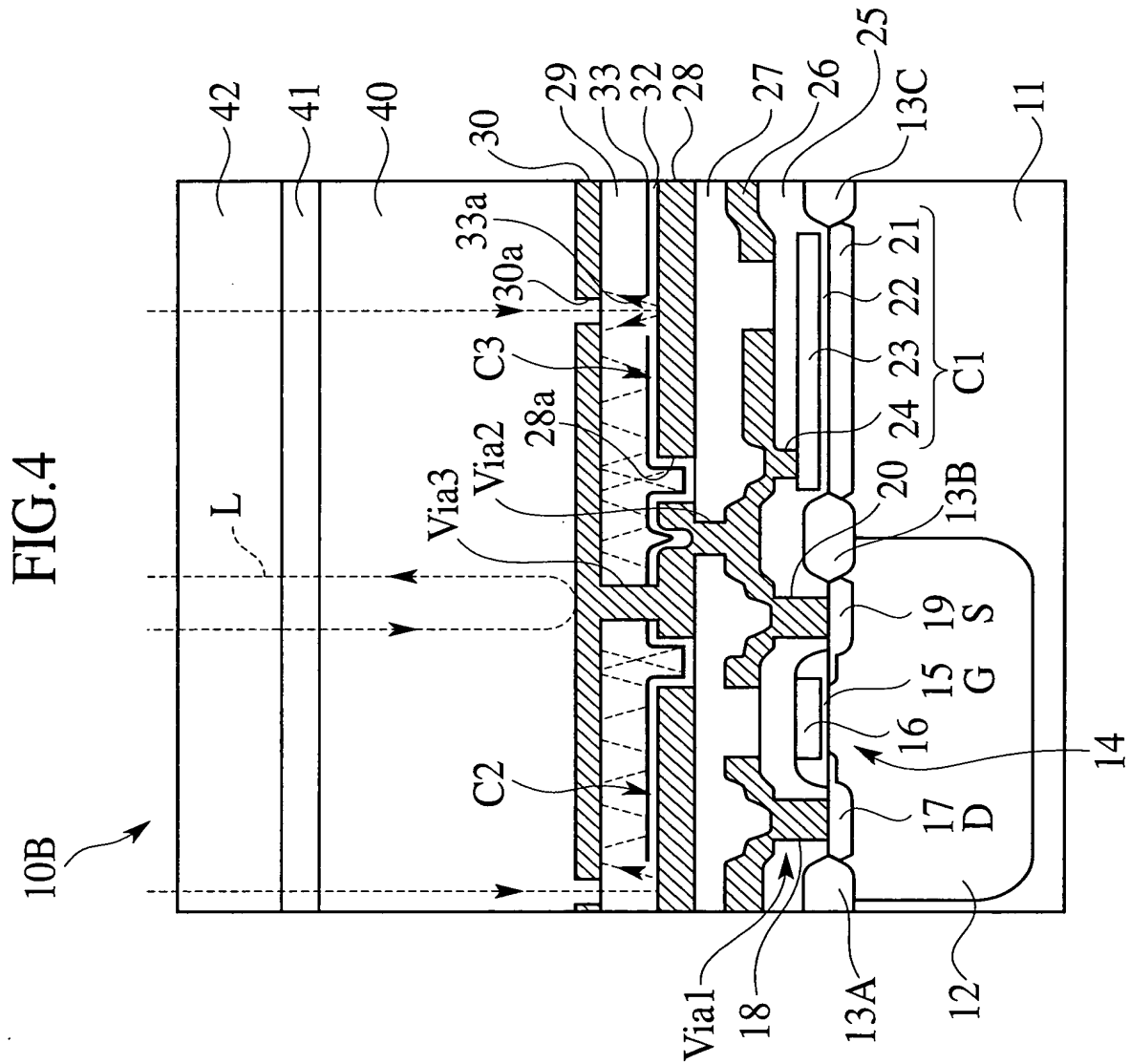


FIG. 3
PRIOR ART



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FIG.5A

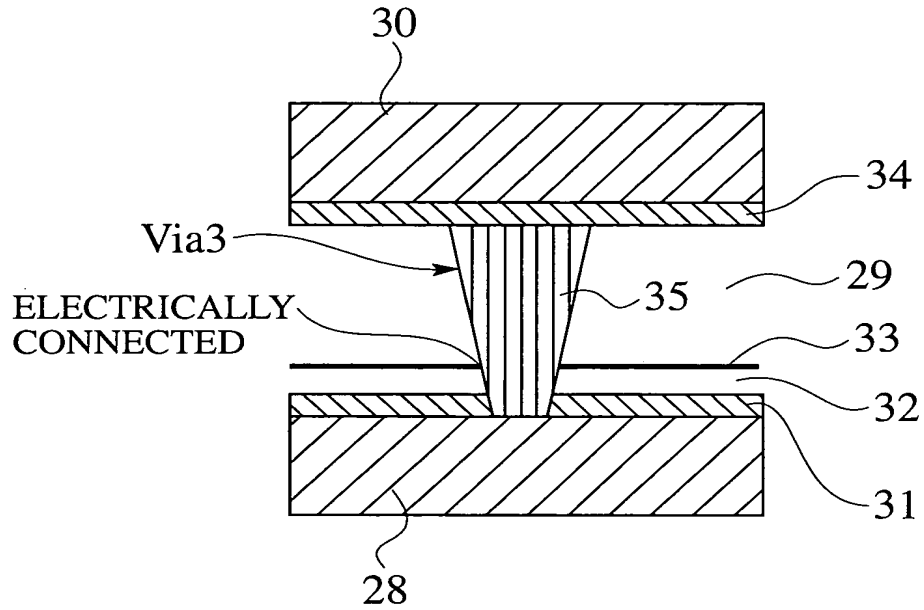
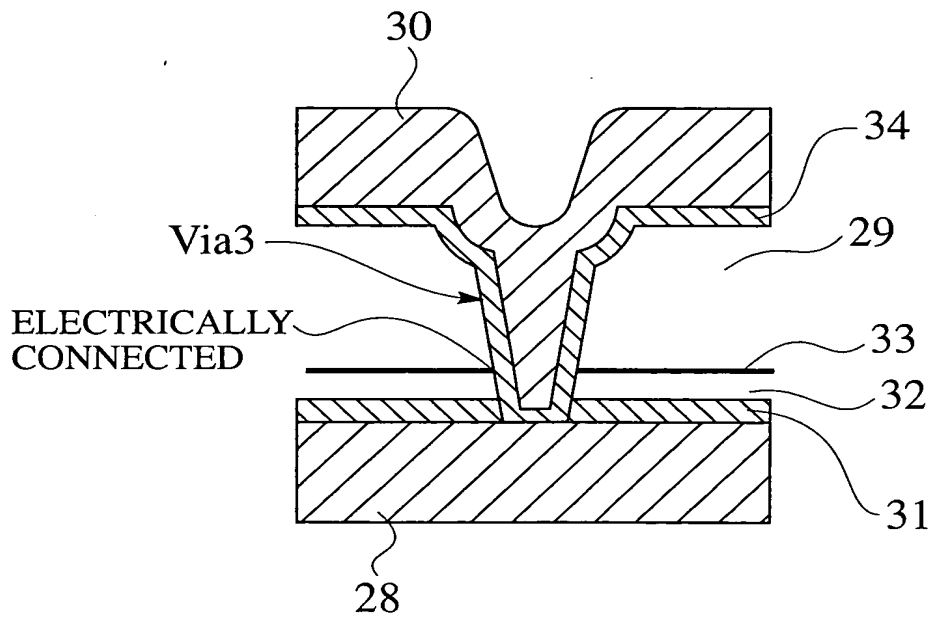
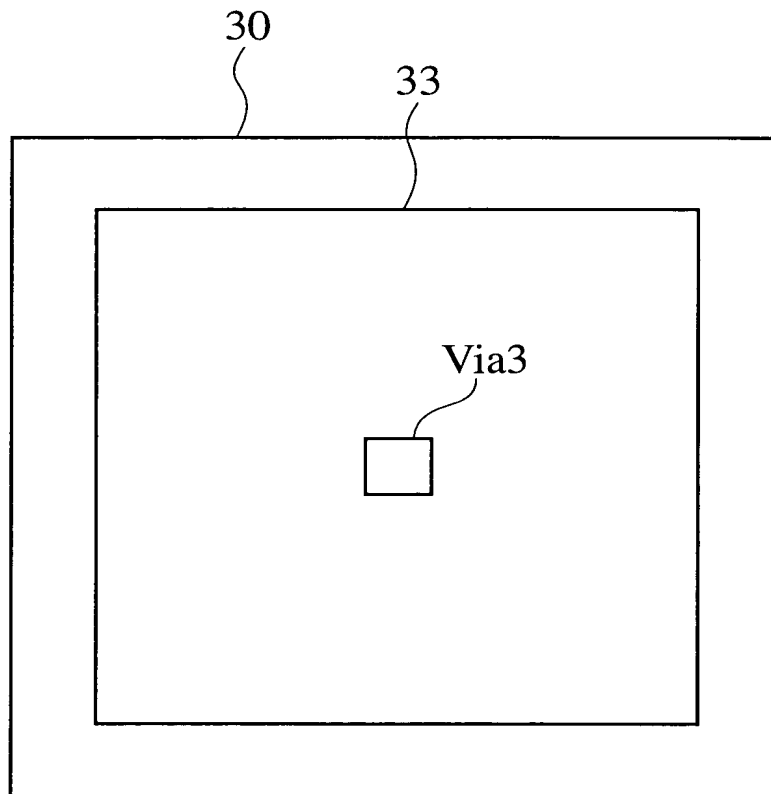


FIG.5B



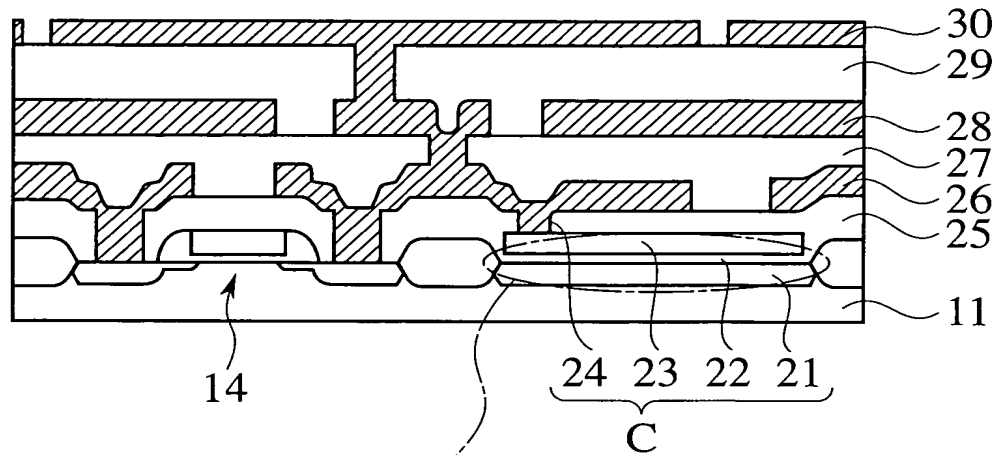
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FIG.6



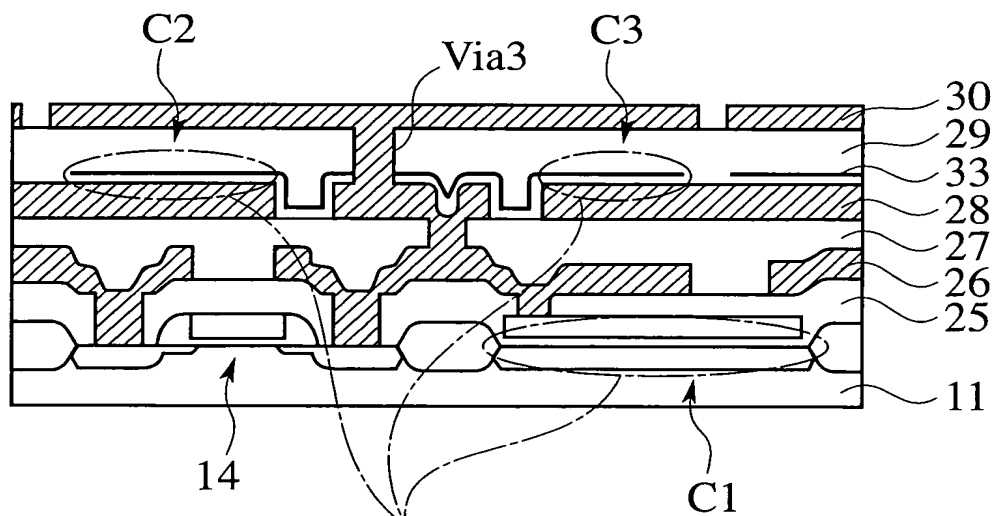
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FIG.7A
PRIOR ART



ONE LOCATION SERVING
AS STORAGE CAPACITOR

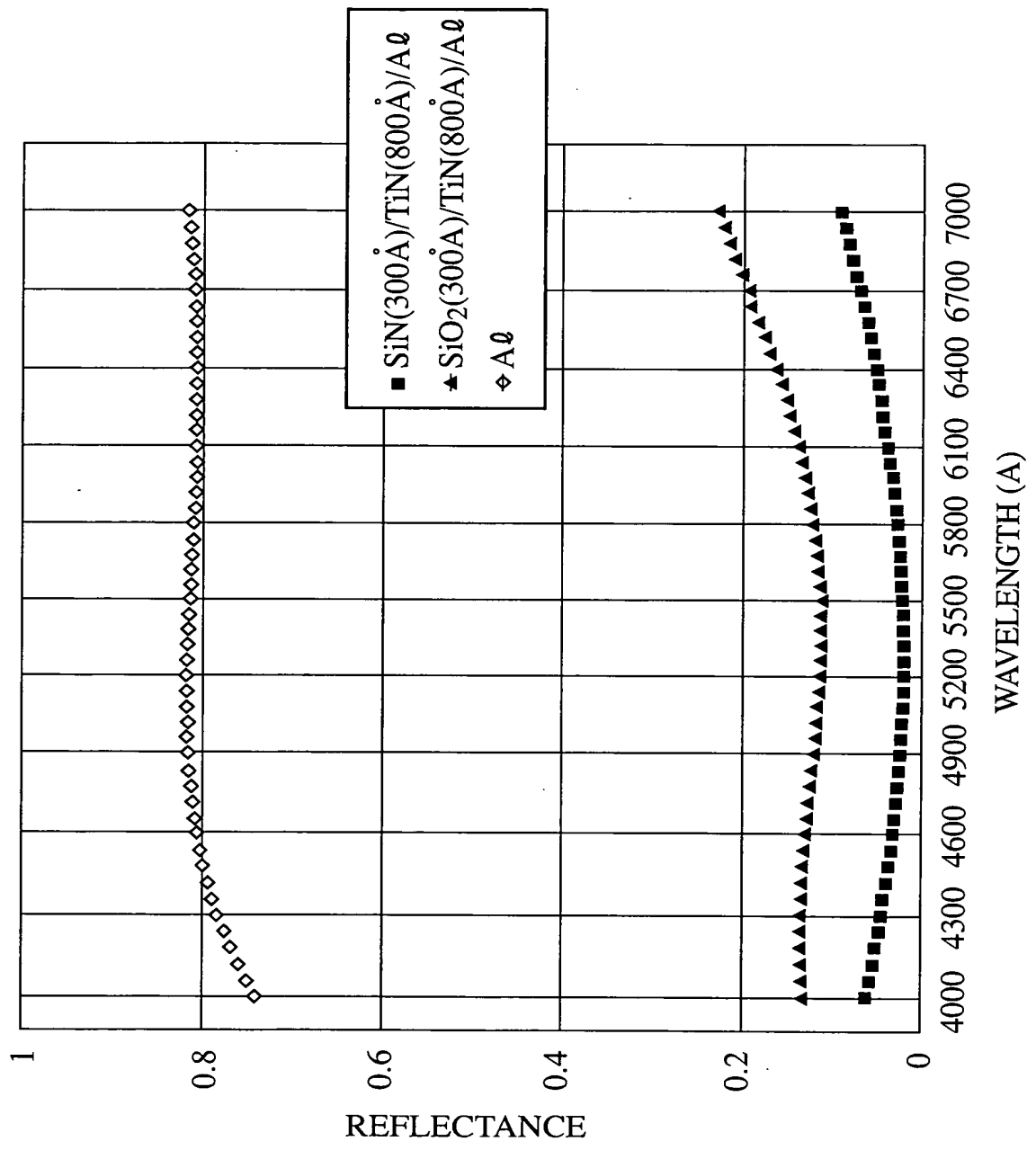
FIG.7B



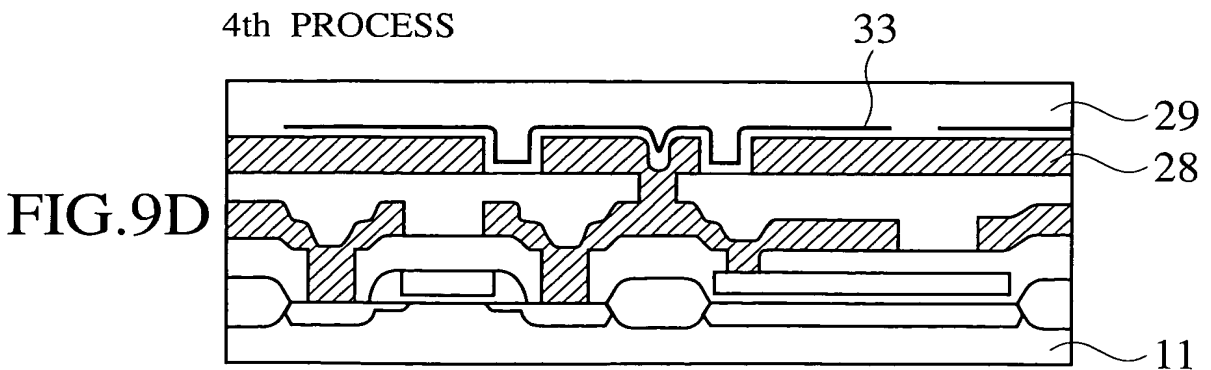
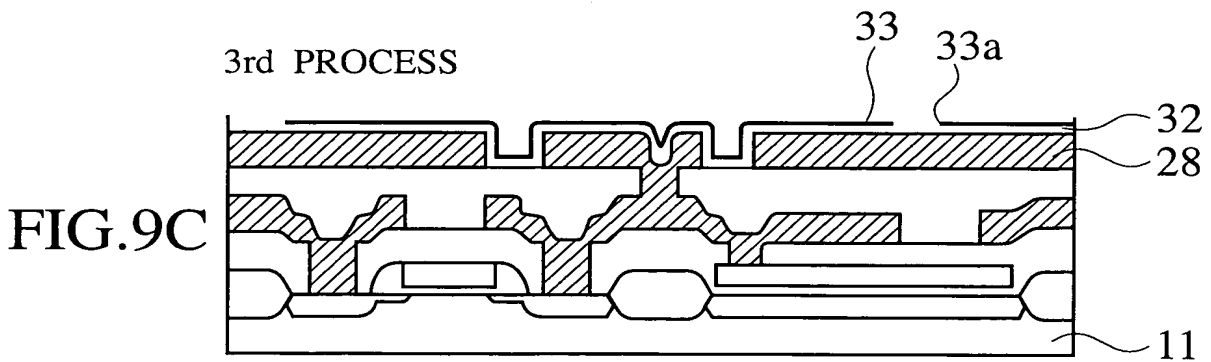
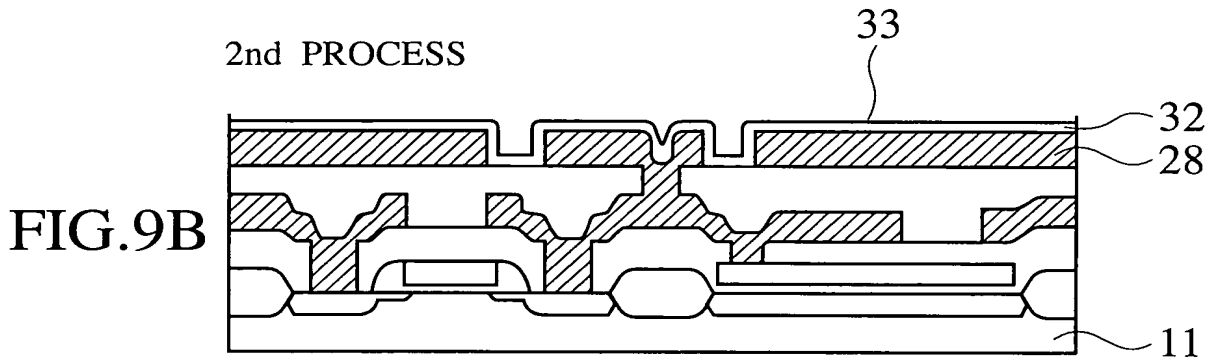
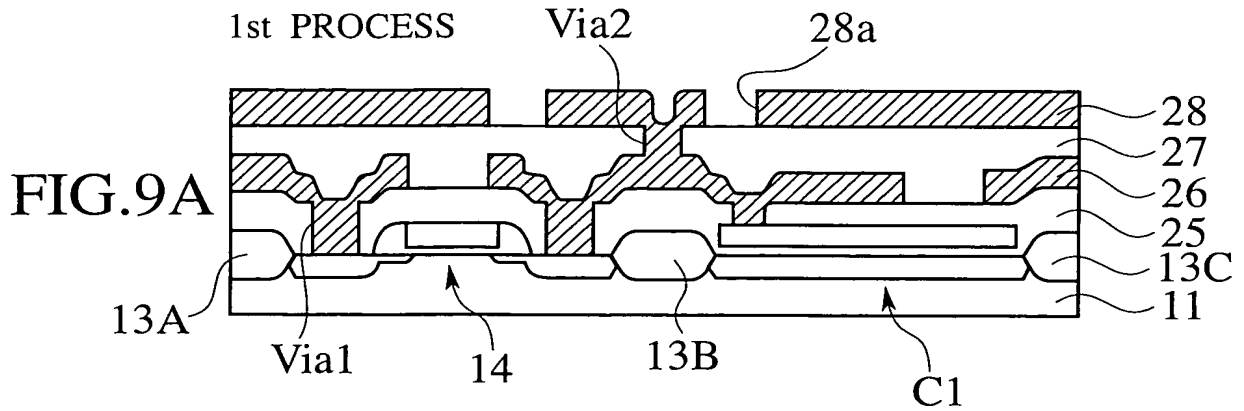
THREE LOCATIONS SERVING
AS STORAGE CAPACITORS

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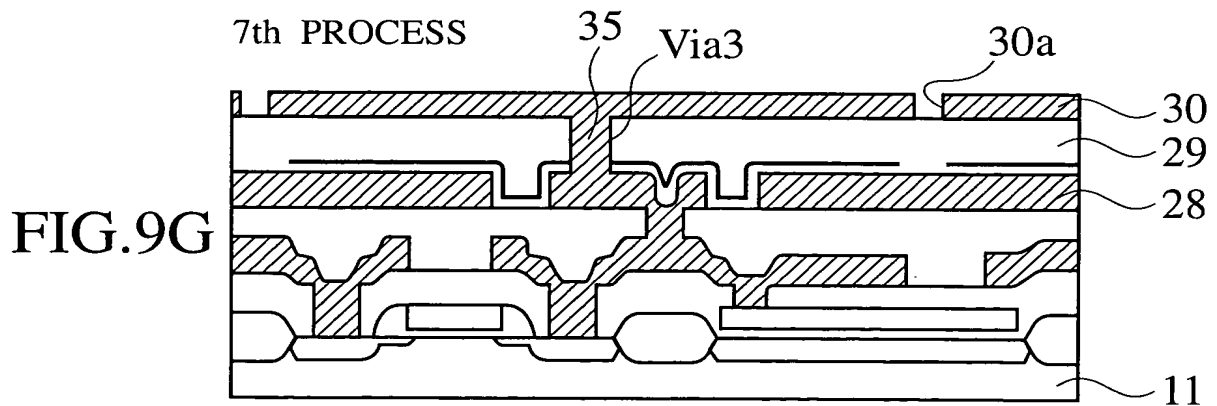
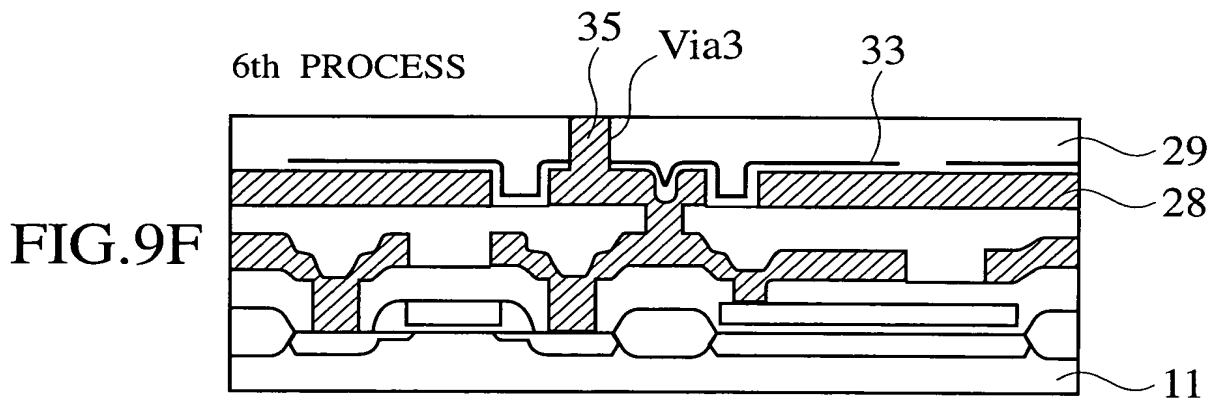
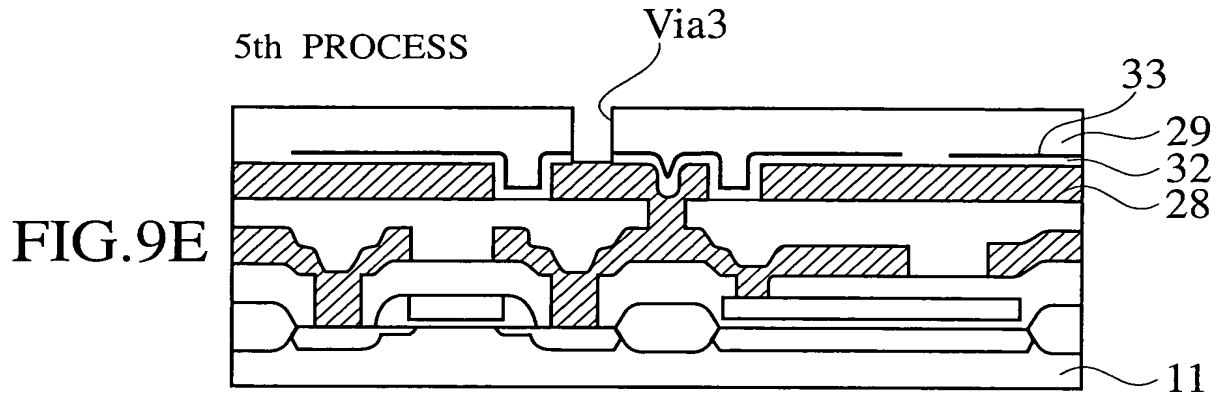
FIG.8



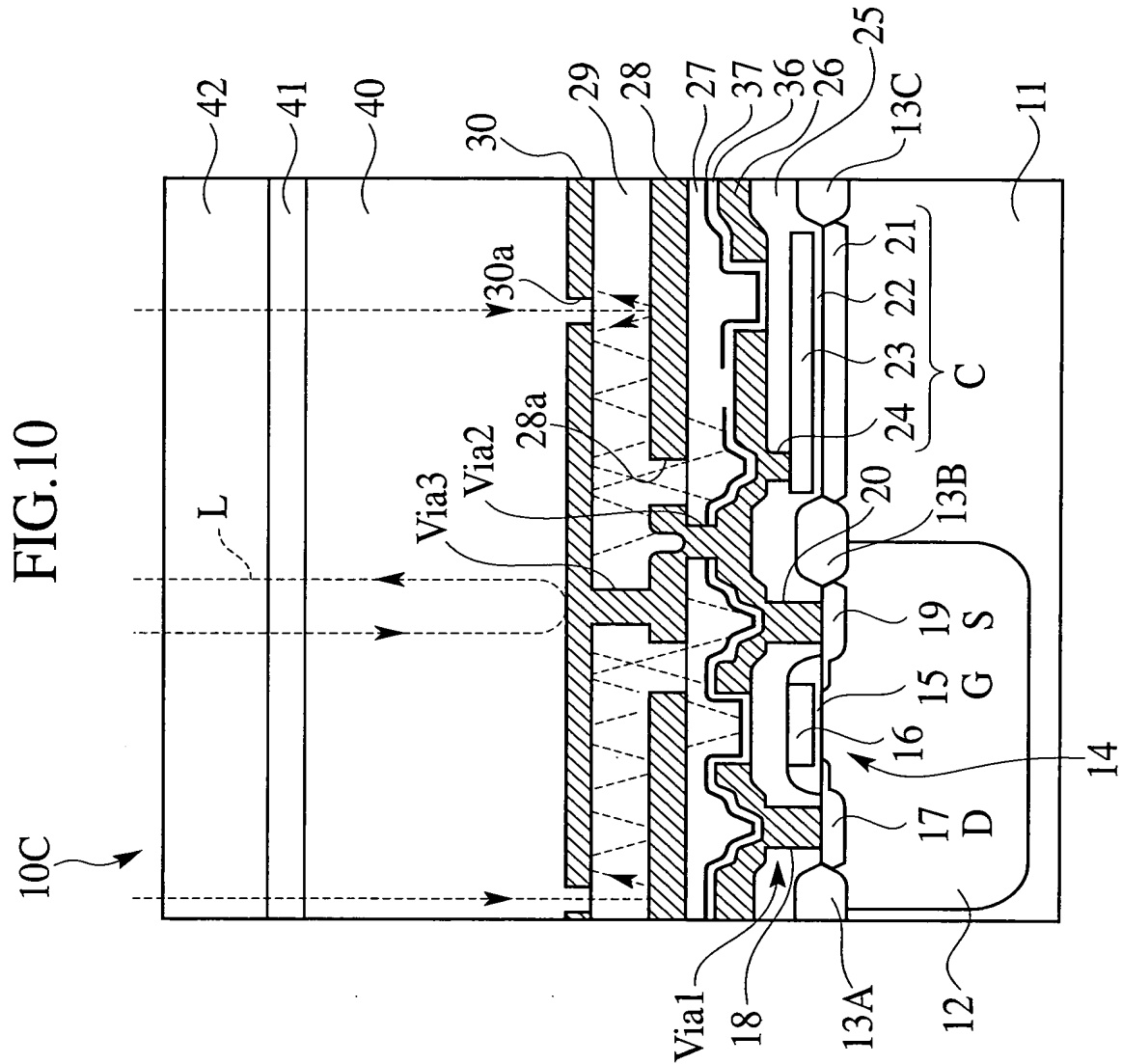
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